

APPLICATION NO. 09/591683	CONT/PRIOR D	CLASS 709 716	SUBCLASS 4	ART UNIT 2123 2825	EXAMINER LIA
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[illegible]

JOE BING SHENG Tseng

Emulation system with multiple asynchronous clocks

PTO-2040
12/89

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	(Primary Examiner) (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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